

Amendments of the Specification

Please replace Paragraph [0079] with the following amended Paragraph [0079]:

[0079] The flow chart in FIG. 18 shows yet another method according to the present invention. This method preferably is implemented on a programmable logic device including a clock network, as shown in step 1810. The clock network preferably comprises multiple branches that provide clock signals to different regions or quadrants in the programmable logic device. Step 1820 queries whether, for a particular time period, (or for a particular region of the PLD (not shown)) a portion or branch of the clock network may be turned OFF. Step 1830 shows that, if the answer to the query in step 1820 is NO, then the method may proceed to the next portion or branch of the clock network (or, alternatively, may evaluate a different time period for the same or different branch). Step 1840 shows if the portion of the clock network may be turned OFF, then, where permitted by speed and routing considerations (as shown in steps ~~1850 and~~ 1860 and 1870) that portion of the clock network should be turned OFF.